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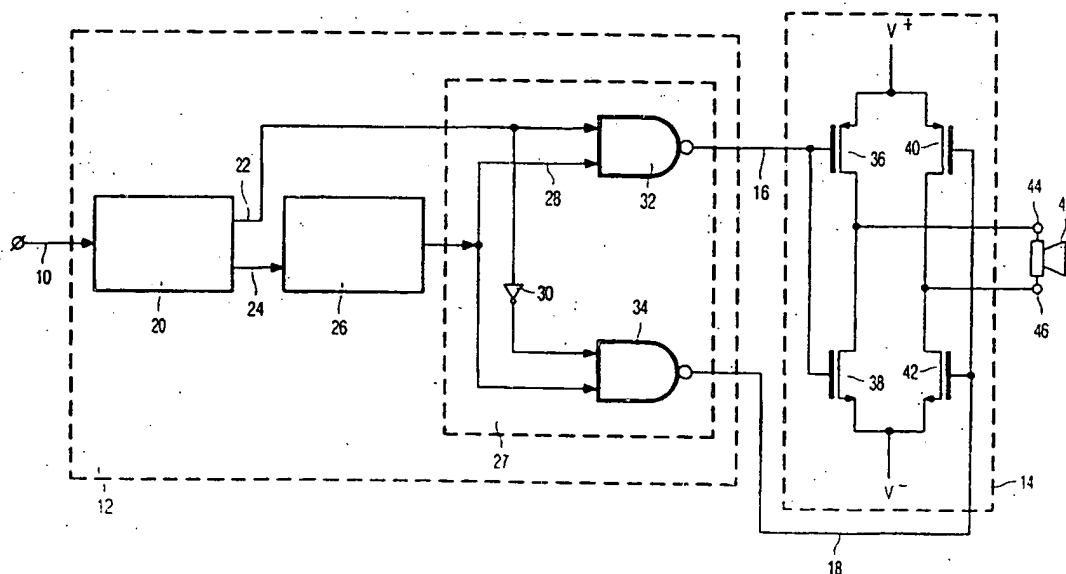
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(54) Title: DEVICE FOR AMPLIFYING SIGNALS



(57) Abstract

The device for amplifying input signals (10) comprises a control stage (12) and a switching bridge amplifier (14), which is coupled to said control stage via at least first and second control signals (16, 18). The bridge amplifier (14) can be switched in at least two states, in dependence upon the control signals (16, 18). The control stage (12) is embodied so as to control only a single substantially passive state of the bridge amplifier (14), as a result of which relatively small switching losses occur.

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Device for amplifying signals

The invention relates to a device for amplifying input signals, comprising a control stage and a switching bridge amplifier, which is coupled to said control stage via at least first and second control signals, the bridge amplifier being switchable in at least two states in dependence upon the control signals.

5 The invention also relates to an audio system and a hearing aid provided with such a device.

Such a device is disclosed in US-A 5 387 875. In audio systems and hearing aids, such devices are often used to amplify audio signals. The device known from said United States patent document comprises an oscillator for generating a triangular reference signal, and
10 first and second comparators for generating, respectively, a first and a second control signal. An input signal to be amplified is converted to the first control signal in the first comparator by comparing the input signal with the reference signal. Similarly, the input signal inverted by an inverter is converted to the second control signal in the second comparator by comparing the inverted input signal with the reference signal. Subsequently, the input signal is amplified
15 by supplying the control signals to a switching bridge amplifier composed of four switches.

The switching bridge amplifier incorporated in the known device may be in four different states. These states are diagrammatically shown in Figs. 2A through 2D. The Figs. 2A and 2B show two active states of the bridge amplifier, wherein the bridge amplifier supplies power to a load 48. The Figs. 2C and 2D show two passive states of the bridge
20 amplifier, wherein the bridge amplifier does not supply power to the load 48. In the known device, all four states of the switching bridge amplifier are controlled by the control stage.

In the known device, relatively much power is dissipated in the bridge amplifier.

It is an object of the invention to provide a device of the type mentioned in the
25 opening paragraph, in which relatively little power is dissipated in the bridge amplifier.

To achieve this, the device in accordance with the invention is characterized in that the control stage is embodied so as to control only a single substantially passive state of the bridge amplifier. In the known device, both passive states of the switching bridge amplifier, as shown in Figs. 2C and 2D, are controlled by the control stage.

The invention is based on the recognition that the relatively high power dissipation in the known device is caused by the large number of switching operations performed by the switches in the bridge amplifier. The switching operation performed by a switch causes a small amount of power to be dissipated in the switch. These so-called switching losses are reduced in the device in accordance with the invention by controlling the bridge amplifier in such a manner that it can only be in one of the two passive states. The other passive state is not controlled. As a result, the switches in the bridge amplifier have to perform fewer switching operations, so that less power is dissipated.

An embodiment of the device in accordance with the invention is characterized in that the control stage comprises a conversion unit for converting the input signals into sign and magnitude-coded signals, said control stage also comprising a pulse width modulator for converting the magnitude of the coded signals to pulse width-modulated signals, and the control stage further comprising a generator for generating the control signals from the pulse width-modulated signals and the sign of the coded signals. It has been found that the control signals can be readily generated in this manner.

A further embodiment of the device in accordance with the invention is characterized in that the pulse width-modulated signals comprise at least bivalent signals, the generator being embodied so as to control the passive state of the bridge amplifier when the pulse width-modulated signals have a substantially low value. By virtue thereof, a simple implementation of the generator is achieved.

These and other aspects of the invention will be apparent from and elucidated with reference to the embodiments described hereinafter.

In the drawings:

Fig. 1 shows a diagram of an example of a device in accordance with the invention.

Figs. 2A through 2D show a number of states of a switching bridge amplifier by means of which the operation of the device in accordance with the invention will be explained.

In the Figures, like reference numerals refer to like parts.

Fig. 1 diagrammatically shows an example of a device in accordance with the invention. The device comprises a control stage 12 and a switching bridge amplifier 14, which is coupled to said control stage via a first control signal 16 and a second control signal 18. An input signal 10 to be amplified is converted by the control stage 12 to the first and the second

control signals 16 and 18. By controlling the bridge amplifier 14 by these control signals 16 and 18, an input signal amplified by the device is obtained between two outputs 44 and 46. The input signal 10 may be, for example, an audio signal which, after it has been amplified, can be converted into sound by a loudspeaker 48 arranged between the outputs 44 and 46.

The control stage 12 comprises a conversion unit 20, a pulse width modulator 26 and a generator 27. In the conversion unit 20, the input signal 10 is converted to a sign and magnitude-coded signal, whereafter the sign 22 and the magnitude 24 of the coded signal are separated from each other. The magnitude 24 of the coded signal is subsequently converted to a pulse width-modulated signal 28 in the pulse width modulator 26. In the generator 27, the control signals 16 and 18 are generated from the pulse width-modulated signal 28 and the sign 22 of the coded signals.

The generator 27 comprises first and second NAND gates 32 and 34, and an inverter 30. By presenting the pulse width-modulated signal 28 and the sign 22 of the coded signals at the inputs of the first NAND gate 32, the first control signal 16 is formed at an output of the first NAND gate 32. The second control signal 18 is formed at an output of the second NAND gate 32 by presenting, at the inputs of the second NAND gate 34, the pulse width-modulated signal 28 and the sign 22 of the coded signals, which is inverted by the inverter 30.

The switching bridge amplifier 14 comprises four field effect transistors 36, 38, 40 and 42. Here, these transistors function as switching elements. The transistors 36 and 40 are so-called n-channel field effect transistors, and the transistors 38 and 42 are so-called p-channel field effect transistors. The sources of the transistors 36 and 40 are connected to a relatively high supply voltage V^+ . The sources of the transistors 38 and 42 are connected to a relatively low supply voltage V^- . The drains of the transistors 36 and 38 are connected to each other and to the output 44. The drains of the transistors 40 and 42 are connected to each other and to the output 46. The gates of the transistors 36 and 38 are connected to the first control signal 16. The gates of the transistors 40 and 42 are connected to the second control signal 18.

If the first control signal 16 has a logic 1 value, then the transistor 36 is turned on, i.e., there is no conduction between the drain and the source of this transistor, while the transistor 38 is turned off, i.e., the drain and the source of this transistor are conductively interconnected. If the second control signal 18 has a logic 1 value, then the transistor 40 is turned on, i.e., there is no conduction between the drain and the source of this transistor, while the transistor 42 is turned off, i.e., the drain and the source of this transistor are conductively interconnected. At these values of the control signals 16 and 18, the bridge amplifier 14 is in

the state shown in Fig. 2C. Analogous thereto, the bridge amplifier 14 is in the state shown in Fig. 2D if the control signals 16 and 18 both have a logic 0 value. The bridge amplifier 14 is in the state shown in Fig. 2A if the first control signal 16 has a logic 0 value, while the second control signal 18 has a logic 1 value. Finally, the bridge amplifier 14 is in the state shown in Fig. 2B if the first control signal 16 has a logic 1 value while the second control signal 18 had a logic 0 value.

The construction of the control stage 12 shown in Fig. 1 causes the switching bridge amplifier 14 to be controlled by the control stage in such a manner that, in the case of a positive input signal 10, the bridge amplifier 14 is alternately in the states shown in Figs. 2B and 2C, while in the case of a negative input signal 10, the bridge amplifier 14 is alternately in the states shown in Figs. 2A and 2C. If the input signal 10 is zero, then the bridge amplifier 14 is in the passive state shown in Fig. 2C. At none of the values of the input signal 10, the bridge amplifier is in the passive state shown in Fig. 2D.

It will be obvious to those skilled in the art that the construction of the generator 27, as shown in Fig. 1, is only one of a large number of possible constructions. Firstly, the logic function obtained by means of AND gates can also be obtained by means of OR gates. In addition, it is possible to embody the generator 27 in such a way that the control signals 16 and 18 are exchanged. Finally, it is also possible to control the bridge amplifier 14 in such a manner that, instead of the passive state shown in Fig. 2C, the passive state shown in Fig. 2D is employed.

The device shown in Fig. 1 can particularly suitably be used in audio systems and hearing aids.

In the Figs. 2A through 2D, a number of states of the switching bridge amplifier 14 are shown. In Fig. 2A, a first active state of the bridge amplifier 14 is shown. Here, the switches 36 and 42 are turned off, and the switches 38 and 40 are turned on. As a result thereof, a current $+i$ flows through a load 48 provided between the outputs 44 and 46. In Fig. 2B, a second active state of the bridge amplifier 14 is shown, wherein the switches 36 and 42 are turned on and the switches 38 and 40 are turned off. As a result, a current $-i$ flows through the load 48. In Fig. 2C, a first passive state of the bridge amplifier 14 is shown. In this state, the switches 36 and 40 are turned on and the switches 38 and 42 are turned off. As a result, the outputs 44 and 46 are both connected to the relatively low supply voltage V^- , so that no current flows through the load 48. Finally, in Fig. 2D, a second passive state of the bridge amplifier 14 is shown, wherein the switches 36 and 40 are turned off and the switches 38 and 42 are turned on. As a result, the outputs 44 and 46 are both connected to the relatively high supply voltage

V^+ , so that, also in this state, no current flows through the load 48. In the device in accordance with the invention, the switching bridge amplifier 14 is controlled in such a manner that only one of the passive states shown in Figs. 2C and 2D occurs.

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1. A device for amplifying input signals (10), comprising a control stage (12) and a switching bridge amplifier (14), which is coupled to said control stage via at least first and second control signals (16, 18), the bridge amplifier (14) being switchable in at least two states in dependence upon the control signals (16, 18), characterized in that the control stage (12) is embodied so as to control only a single substantially passive state of the bridge amplifier (14).
2. A device as claimed in claim 1, characterized in that the control stage (12) comprises a conversion unit (20) for converting the input signals (10) into sign and magnitude-coded signals, said control stage also comprising a pulse width modulator (26) for converting the magnitude (24) of the coded signals to pulse width-modulated signals (28), and the control stage (12) further comprising a generator (27) for generating the control signals (16, 18) from the pulse width-modulated signals (28) and the sign (22) of the coded signals.
3. A device as claimed in claim 2, characterized in that the pulse width-modulated signals (28) comprise at least bivalent signals, the generator (27) being embodied so as to control the passive state of the bridge amplifier (14) when the pulse width-modulated signals (28) have a substantially low value.
4. An audio system provided with a device for amplifying signals in accordance with any one of the preceding claims.
5. A hearing aid provided with a device for amplifying signals as claimed in any one of the claims 1 through 3.



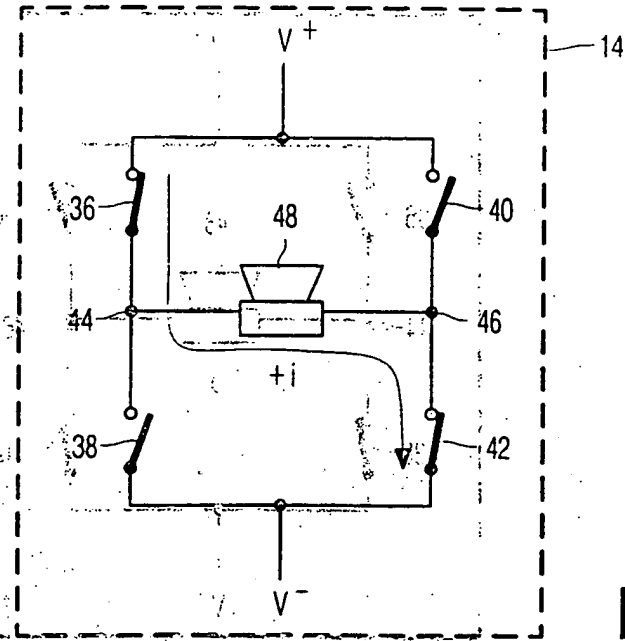


FIG. 2A

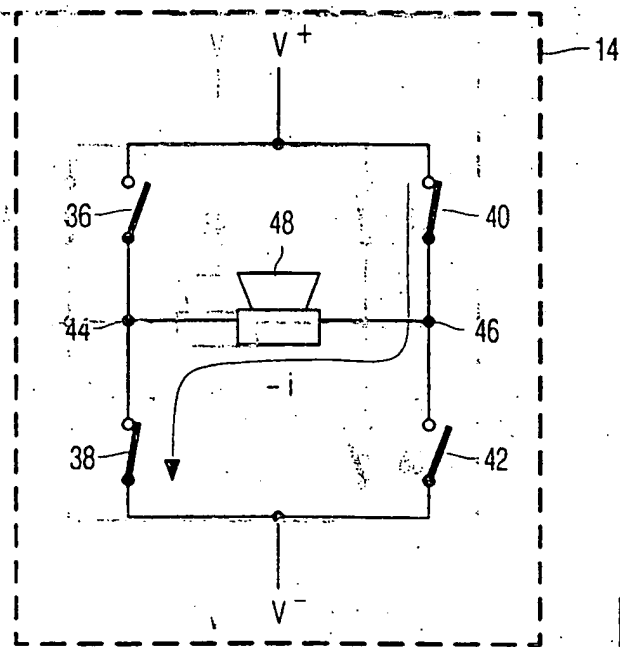


FIG. 2B

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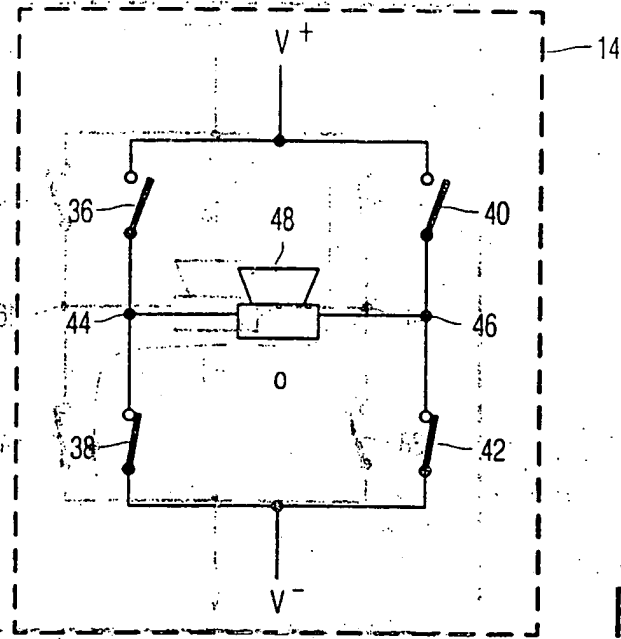


FIG. 2C

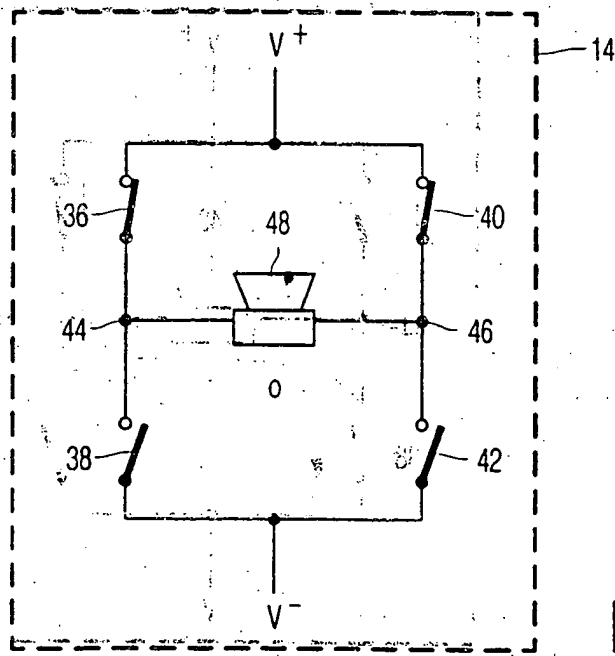


FIG. 2D

INTERNATIONAL SEARCH REPORT

International Application No.

PCT/EP 99/09205

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H03F3/217

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 442 317 A (STENGEL ROBERT E) 15 August 1995 (1995-08-15) the whole document	1-4
X	US 5 506 493 A (STENGEL ROBERT E) 9 April 1996 (1996-04-09) the whole document	1-4
A	US 5 387 875 A (TATENO MAKOTO) 7 February 1995 (1995-02-07) cited in the application the whole document	1-5

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Date of the actual completion of the international search

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Patent document cited in search report		Publication date		Patent family member(s)	Publication date
US 5442317	A	15-08-1995	US	5506493 A	09-04-1996
			AU	5667194 A	08-06-1994
			WO	9411799 A	26-05-1994
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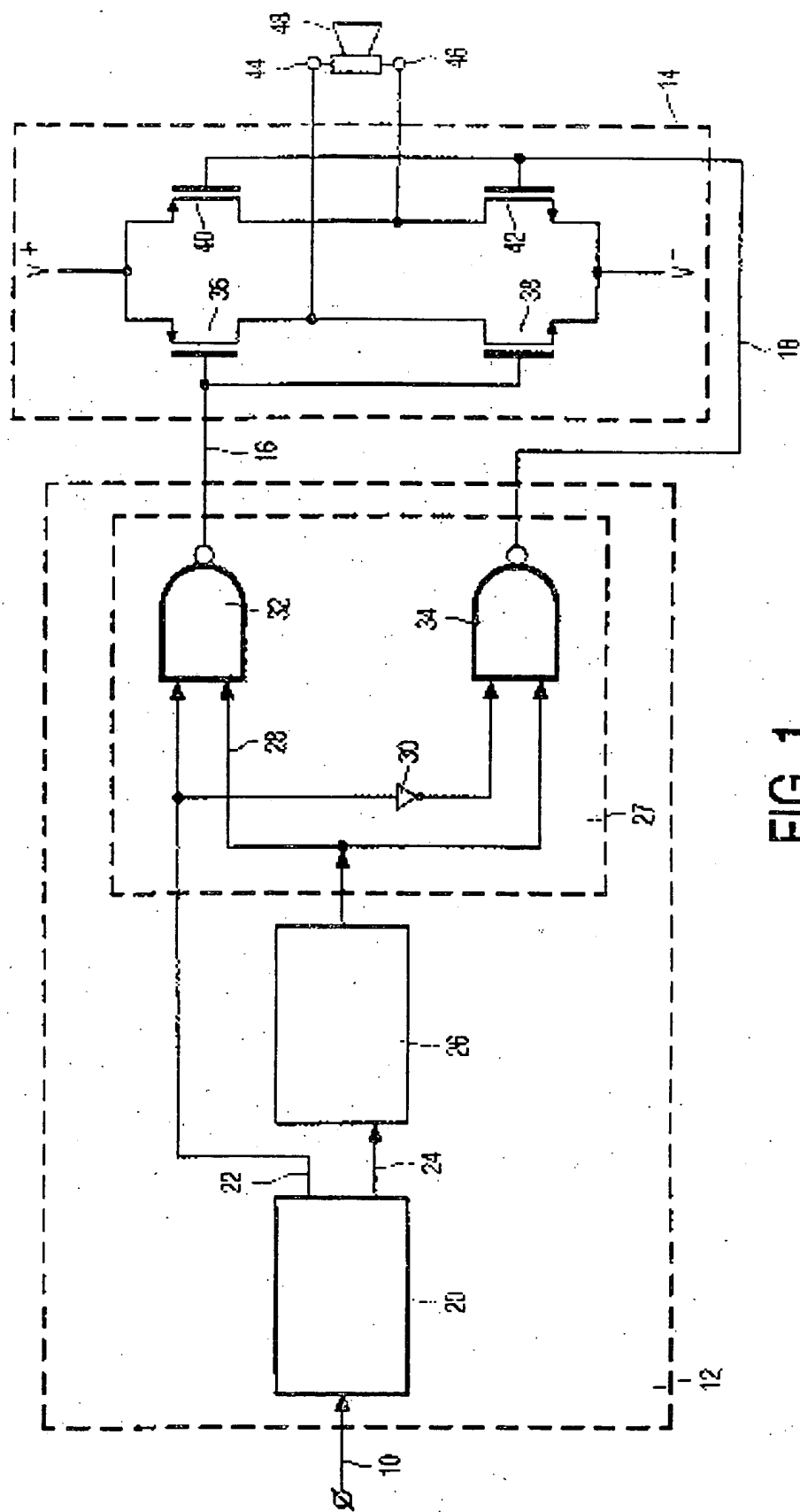


FIG. 1

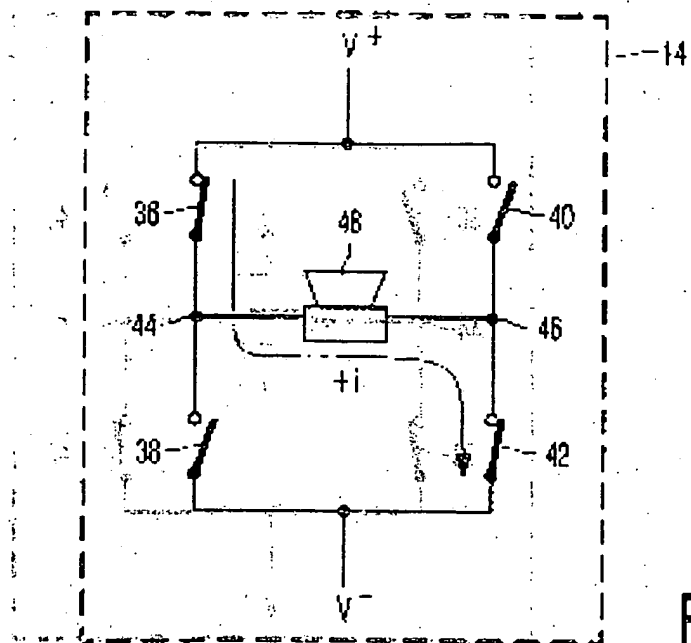


FIG. 2A

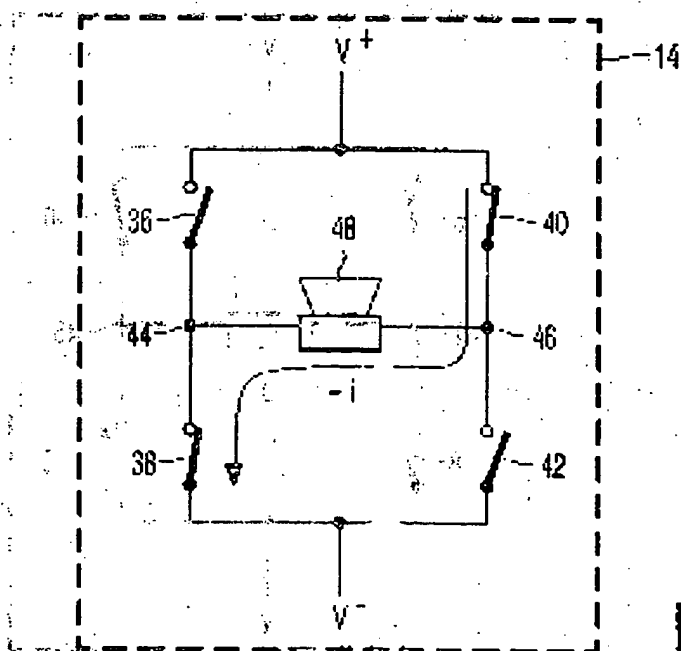


FIG. 2B

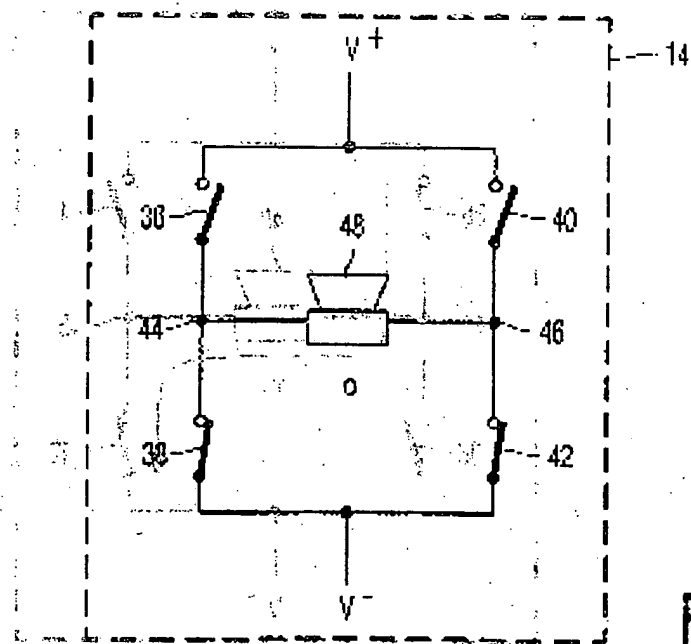


FIG. 2C

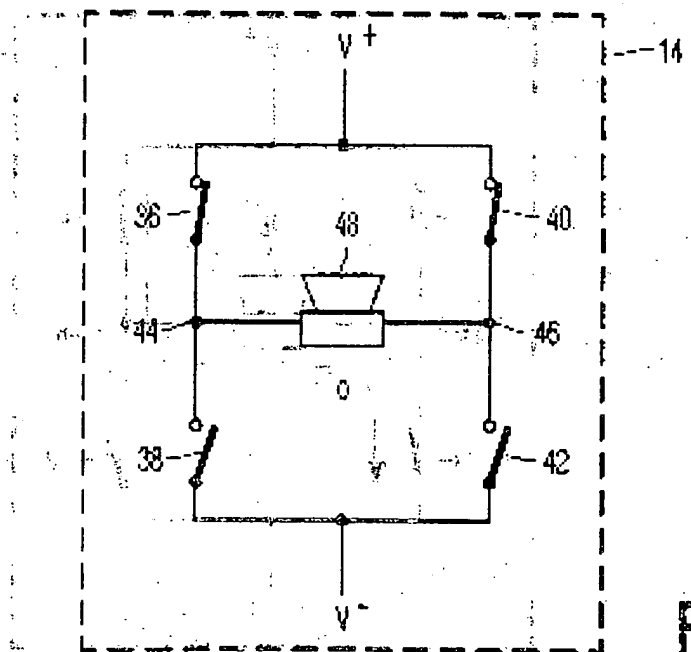


FIG. 2D

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